Design your own Application Carrier Board using phyCORE-SOM

Organised & Supported by **PHYTEC Embedded Pvt Ltd**

&

Open Source Community **RuggedBOARD**

Workshop Details

DAY-1

Specification & BOM Selection

- Make specification of the ACB
- Select the right SOM according to Application, OS, Stack & SW-APP
- Selecting right BOM parts for ACB
- Design Block Diagram for ACB

DAY-2

Schematic Blocks & Layout Guidelines

- Preparing Pin Muxing for ACB
- Schematic for the ACB
- Important guideline for PCB Layout/Design
- Guidelines for PCB FAB

DAY-3

BSP Modifications (Bootloader & Kernel)

- Understanding Pin-Muxing in Linux Kernel
- Customising Bootloader for ACB.
- Boot Custom Linux on ACB by modifying Device-Tree

ACB: Application Carrier BOARD

Workshop Details

DAY-4

BSP Modifications (Yocto Linux)

- Understanding Layers in Yocto BSP
- Configure Stack level packages needed for specific Application.
- Building custom Linux OS Image

DAY-5

Application Development & Demos

- Applications to validate the hardware interfaces & device on ACB.
- How to use Yocto SDK for application building
- Demo- Industrial Control & Automation

Who Should Attend ?

Hardware Engineers
Software / Firmware Engineers
System Engineers
Architects & Principal Engineers
Managers & CxO's

Training Details

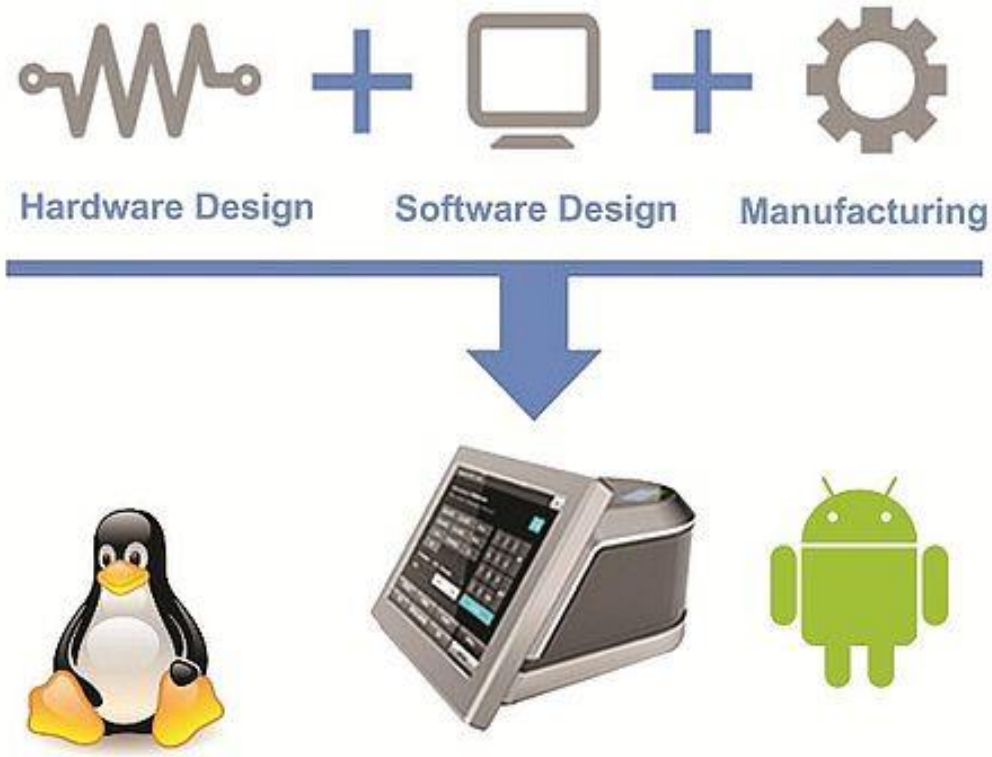
Duration: 5 Days [7:00 – 9:00 PM]
Fees: 10K INR
Training Mode: Online

Application Verticals

Industry-4.0
Transportation & eMobility
Medical & Healthcare
Smart Energy, Smart Cities, Smart Agri

Rapid Prototyping & Manufacturing

1. Specifications
2. BOM Selection
3. Schematic
4. PCB Layout
5. PCB Fabrication
6. PCB Assembly
7. Hardware Validation
8. Proto Housing
9. S/W BSP Development



Workshop Registration

Registration Link ...

eMail
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PHYTEC Embedded would be preferred partner as one stop solution provider for your Product Design, Development & Manufacturing. [sales@phytec.in]